# EVERLIGH

# DATASHEET

# **6 PIN DIP SCHMITT TRIGGER PHOTOCOUPLER** H11LX Series



#### Features:

- High data rate, 1MHz typical (NRZ)
- Free from latch up and oscillation throughout voltage and temperature ranges.
- Microprocessor compatible drive
- Logic compatible output sinks 16mA at 0.4V maximum
- Guaranteed on/off threshold hysteresis
- Wide supply voltage capability, compatible with all popular logic systems
- High isolation voltage between input and output (Viso=5000 V rms)
- Compact dual-in-line package
- •The product itself will remain within RoHS compliant version Compliance with EU REACH
- UL and cUL approved(No. E214129)
- VDE approved (No. 132249)
- SEMKO approved
- NEMKO approved
- DEMKO approved
- FIMKO approved
- CQC approved

### Description

The H11LX series of devices each consist of a GaAs infrared emitting diode optically coupled a high speed integrated circuit detector. The output detector incorporates a Schmitt trigger, which provides hysteresis for noise immunity and pulse shaping.

The devices are in a 6-pin DIP package and available in wide-lead spacing and SMD option.

# Applications

- Logic to logic isolator
- Programmable current level sensor
- Line receiver eliminate noise and transient problems
- AC to TTL conversion square wave shaping
- Digital programming of power supplies
- Interfaces computers with peripherals

Pin Configuration

Schematic

- 1. Anode
- 2. Cathode
- 3. No Connection
- 4. Vo 5. GND
- 6. V<sub>CC</sub>

Iruth Table					
Input	Output				
Н	L				
L	Н				

# Absolute Maximum Ratings (Ta=25°C)

	Parameter	Symbol	Rating	Unit
	Forward current	١ <sub>F</sub>	60	mA
Input	Reverse voltage	V <sub>R</sub>	6	V
	Power dissipation	P <sub>D</sub>	120	mW
- Output	V <sub>45</sub> Allowed Range	Vo	0 to 16	V
	V <sub>65</sub> Allowed Range	V <sub>CC</sub>	3 to 16	V
	Output Current	Ι <sub>ο</sub>	50	mA
	Power dissipation	P <sub>D</sub>	150	mW
Total powe	r dissipation	P <sub>tot</sub>	250	mW
Isolation vo	oltage	V <sub>iso</sub>	5000	V rms
Operating	temperature	T <sub>opr</sub>	-55~+100	°C
Storage te	mperature	T <sub>stg</sub>	-55~+125	°C
Soldering t	temperature *2	T <sub>sol</sub>	260	°C

#### Notes:

\*1 AC for 1 minute, R.H.= 40 ~ 60% R.H. In this test, pins 1, 2 & 3 are shorted together, and pins 4, 5 & 6 are shorted together.

\*2 For 10 seconds

# Electro-Optical Characteristics (Ta=25°C unless specified otherwise)

Input						
Parameter	Symbol	Min.	Тур.*	Max.	Unit	Condition
Forward Voltage	V <sub>F</sub>	-	1.15	1.5	V	$I_F = 10 \text{mA}$
Reverse Current	I <sub>R</sub>	-	-	10	μA	$V_R = 5V$
Input capacitance	CJ	-	-	100	pF	V=0, f=1MHz
Output						
Parameter	Symbol	Min.	Тур.*	Max.	Unit	Condition
Operation Voltage Range	V <sub>CC</sub>	3	-	15	V	
Supply Current	I <sub>CC(off)</sub>	-	1.6	5	mA	I <sub>F</sub> =0mA, Vcc=5V
Output Current, High	I <sub>OH</sub>	-	-	100	μA	I <sub>F</sub> =0mA, Vcc=Vo=15V
Isolation Resistance	R <sub>ISO</sub>	10 <sup>11</sup>	-	-	Ω	V <sub>I-O</sub> =500VDC

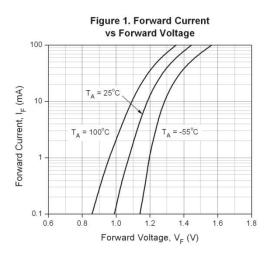
### **Transfer Characteristics**

Parameter	Symbol	Min	Тур.	Max.	Unit	Condition
Supply Current	I <sub>CC(on)</sub>		1.6	5	mA	I <sub>F</sub> =10mA, Vcc=5V
Output Voltage .low	V <sub>OL</sub>			0.4	V	Vcc=5V, $I_F=I_{Fon}(max.)$ , $R_L=270\Omega$
Turn on H11L1		-	-	1.6		
Threshold H11L2	Fon	-	-	10	mA	Vcc=5V, $R_L$ =270 $\Omega$
Current <sup>1</sup> H11L3		-	-	5		
Turn off Threshold Current	I <sub>Foff</sub>	-	1	-	mA	Vcc=5V, $R_L$ =270 $\Omega$
Hysteresis Ratio	I <sub>Foff</sub> /I <sub>Fon</sub>	0.5	-	0.9		Vcc=5V, $R_L$ =270 $\Omega$
Turn on Time	t <sub>on</sub>	-	-	4	μS	
Fall Time	t <sub>r</sub>	-	0.1	-	μS	Vcc=5V, I <sub>F</sub> =I <sub>Fon</sub> ,
Turn off Time	t <sub>off</sub> 4		μS	$R_L=270\Omega$		
Rise Time	t <sub>r</sub>	-	0.1	-	μS	
Data Rate		-	1	-	MHz	

\* Typical values at  $T_a = 25^{\circ}C$ 

<sup>1</sup>. Max.  $I_{F(ON)}$  is the maximum current required to trigger the output. For examples, a 1.6mA maximum trigger current would require the LED to be driven at a current greater than 1.6mA to guarantee the device will turn on. A 10% guard band is recommended to account for degradation of the LED over its lifetime. The maximum allowable LED drive current is 60mA.

# **Typical Electro-Optical Characteristics Curves**



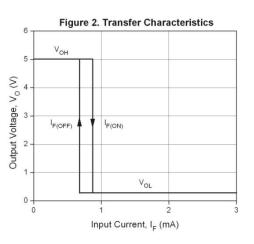
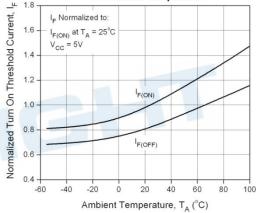
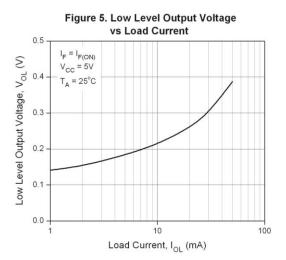
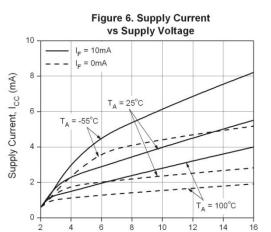


Figure 3. Turn On Threshold Current vs Supply Voltage 1.6 \_<u>u</u> Normalized Turn On Threshold Current, I IF Normalized to: 1.4 I<sub>F(ON)</sub> at V<sub>CC</sub> = 5V  $T_A = 25^{\circ}C$ 1.2 F(ON) 1.0 0.8 F(OFF) 0.6 0.4 0.2 ò 2 6 8 10 12 14 16 Supply Voltage, V<sub>CC</sub>

Figure 4. Turn On Threshold Current vs Ambient Temperature







Supply Voltage, V<sub>CC</sub> (V)

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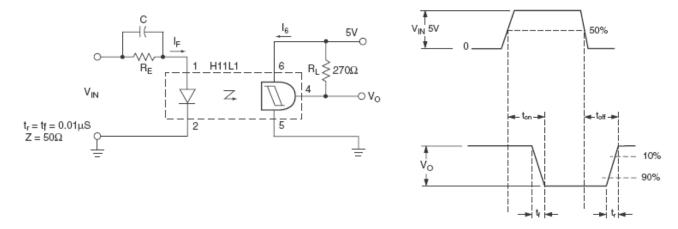


Figure 7. Switching Time Test Circuit & Waveforms

## **Order Information**

### **Part Number**



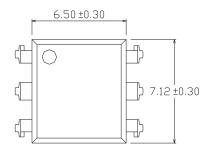
#### Note

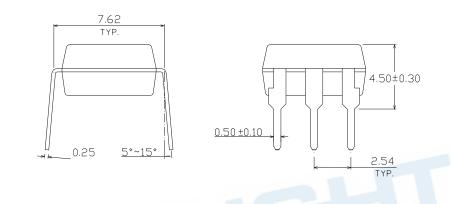
- $\overline{X}$  = Part No. for 1, 2 or 3
- Y = Lead form option (S, S1, M or none)
- Z = Tape and reel option (TA, TB or none).
- V = VDE (optional)

Option	Description	Packing quantity
None	Standard DIP-6	65 units per tube
М	Wide lead bend (0.4 inch spacing)	65 units per tube
S + TA	Surface mount lead form + TA tape & reel option	1000 units per reel
S + TB	Surface mount lead form + TB tape & reel option	1000 units per reel
S1 + TA	Surface mount lead form (low profile) + TA tape & reel option	1000 units per reel
S1 + TB	Surface mount lead form (low profile) + TB tape & reel option	1000 units per reel

# Package Dimension (Dimensions in mm)

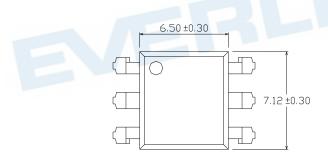
# **Standard DIP Type**

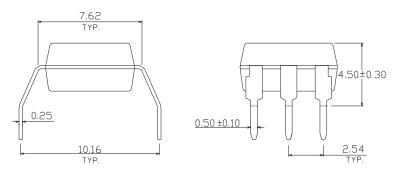




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### **Option M Type**

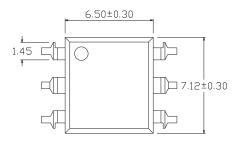


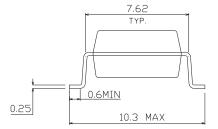


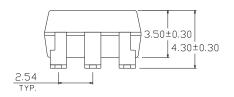
### DATASHEET 6 PIN DIP Schmitt Trigger PHOTOCOUPLER H11LX Series

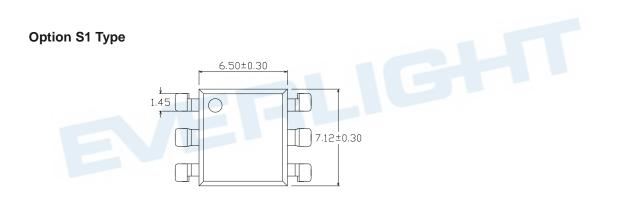
# **EVERLIGHT**

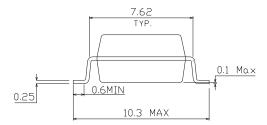
### **Option S Type**

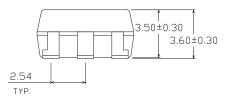








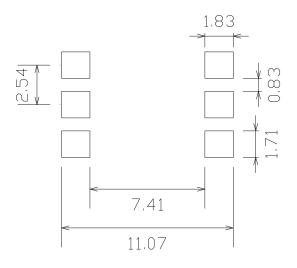




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# **EVERLIGHT**

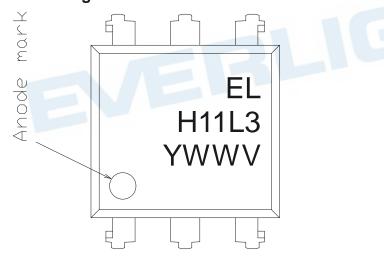
### Recommended pad layout for surface mount leadform



#### Notes

Suggested pad dimension is just for reference only. Please modify the pad dimension based on individual need.

### **Device Marking**



#### Notes

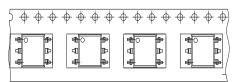
EL	denotes Everlight
H11L3	denotes Device Number
Υ	denotes 1 digit Year code
WW	denotes 2 digit Week code
V	denotes VDE (optional)

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# **Tape & Reel Packing Specifications**

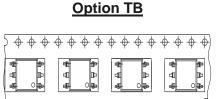
**Tape dimensions** 





Direction of feed from reel





#### Direction of feed from reel



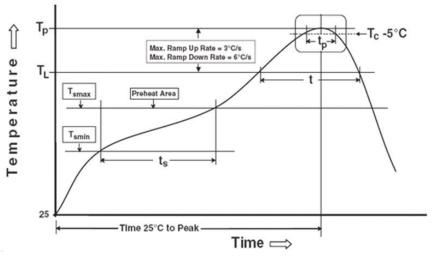
Dimension No.	A	В	Do	D1	E	F
Dimension(mm)	10.8±0.1	7.55±0.1	1.5±0.1	1.5+0.1/-0	1.75±0.1	7.5±0.1
Dimension No.	Ро	P1	P2	t	W	к
Dimension(mm)	4.0±0.15	12±0.1	2.0±0.1	0.35±0.03	16.0±0.2	4.5±0.1



# **Precautions for Use**

#### 1. Soldering Condition

1.1 (A) Maximum Body Case Temperature Profile for evaluation of Reflow Profile



Note:

### Preheat

Temperature min (T<sub>smin</sub>)

Temperature max (T<sub>smax</sub>)

Time ( $T_{smin}$  to  $T_{smax}$ ) ( $t_s$ ) Average ramp-up rate ( $T_{smax}$  to  $T_p$ )

### Other

Liquidus Temperature ( $T_L$ ) Time above Liquidus Temperature ( $t_L$ ) Peak Temperature ( $T_P$ ) Time within 5 °C of Actual Peak Temperature:  $T_P$  - 5°C Ramp- Down Rate from Peak Temperature Time 25°C to peak temperature Reflow times Reference: IPC/JEDEC J-STD-020D

150 °C 200°C 60-120 seconds 3 °C/second max

217 °C 60-100 sec 260°C 30 s 6°C /second max. 8 minutes max. 3 times

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