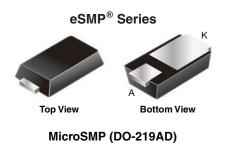
V1PM10

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Vishay General Semiconductor

Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifier



Anode O Cathode

LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS			
I _{F(AV)}	1.0 A		
V _{RRM}	100 V		
I _{FSM}	25 A		
V _F at I _F = 1.0 A (125 °C)	0.58 V		
T _J max.	175 °C		
Package	MicroSMP (DO-219AD)		
Circuit configuration	Single		

FEATURES

- Very low profile typical height of 0.65 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- Low forward voltage drop
- Low power loss, high efficiency
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
 - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications, in commercial, industrial, and automotive applications.

MECHANICAL DATA

Case: MicroSMP (DO-219AD) Molding compound meets UL 94 V-0 flammability rating

Base P/N-M3 - halogen-free, and RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 gualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test **Polarity:** color band denotes the cathode end

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	L V1PM10		
Device marking code		1MB		
Maximum repetitive peak reverse voltage	V _{RRM}	100	V	
Maximum DC forward current	I _{F(AV)}	1.0	А	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	25	А	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +175	°C	

AUTOMOTIVE GRADE Available

> RoHS COMPLIANT HALOGEN FREE

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V1PM10

ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 0.5 A	- T _A = 25 °C		0.58	-	- V
	I _F = 1.0 A		V _E (1)	0.69	0.77	
	I _F = 0.5 A	T _A = 125 °C	VF \.,	0.50	-	
	I _F = 1.0 A			0.58	0.66	
Reverse current	V _R = 70 V	T _A = 25 °C		1	-	μA
	V _R = 100 V		I _R ⁽²⁾	-	50	
	V _R = 70 V	T _A = 125 °C	IR (-/	0.2	-	mA
	V _R = 100 V			0.5	1.5	
Typical junction capacitance	4.0 V, 1 MHz		CJ	100	-	pF

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: pulse width \leq 5 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL V1PM10			
Typical thermal resistance	R _{0JA} ⁽¹⁾⁽²⁾	130	°C/W	
	R _{0JM} ⁽³⁾	20	0/10	

Notes

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$

 $^{(2)}$ Free air, mounted on FR4 PCB, 2 oz. standard footprint, $R_{\theta JA}$ - junction to ambient

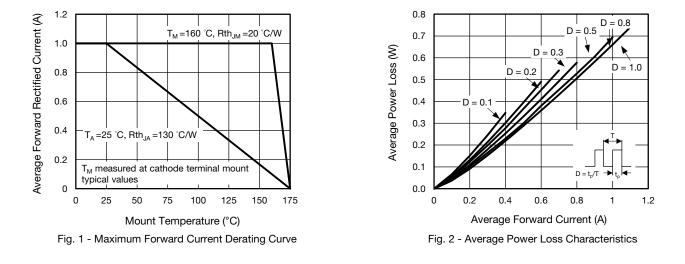
 $^{(3)}$ Mounted on FR4 PCB, 2 oz. standard footprint, $R_{\theta JM}$ - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V1PM10-M3/H	0.006	Н	4500	7" diameter plastic tape and reel	
V1PM10HM3/H ⁽¹⁾	0.006	Н	4500	7" diameter plastic tape and reel	

Note

⁽¹⁾ AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)



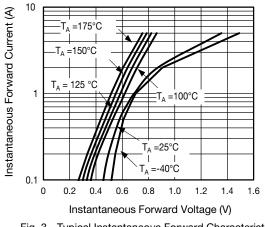
Revision: 03-Aug-2020

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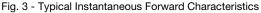
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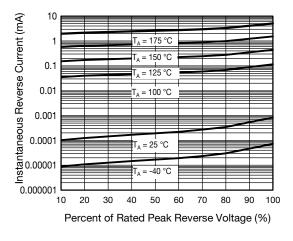
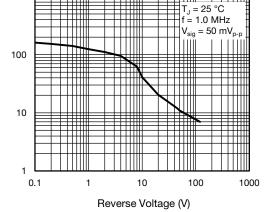


Fig. 4 - Typical Reverse Leakage Characteristics



1000

Junction Capacitance (pF)



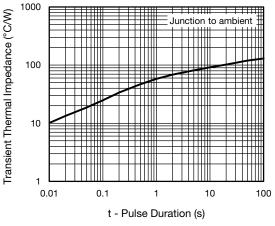


Fig. 6 - Typical Transient Thermal Impedance

MicroSMP (DO-219AD) 0.059 (1.50) 0.030 (0.75) Cathode Band 0.043 (1.10) 0.022 (0.55) ŧ ¥ 0.055 (1.40) 0.039 (0.98) 0.030 (0.75) 0.047 (1.20) 0.031 (0.78) 0.022 (0.55) 4 0.091 (2.30) 0.083 (2.10) 0.106 (2.70) 0.091 (2.30) **Mounting Pad Layout** 0.079 0.032 (2.00) (0.80) 0.029 (0.73) 0.043 0.032 0.025 (0.63) (1.10) . (0.80) ŧ 0.011 (0.27) 0.005 (0.12) 0.020 (0.50) Revision: 03-Aug-2020 Document Number: 87664 3

PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

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