

PESD36VS1UJ-Q Unidirectional ESD protection diode

1 June 2022

1. General description

Unidirectional ElectroStatic Discharge (ESD) protection diode in a flat lead very small SOD323F Surface-Mounted Device (SMD) plastic package designed to protect one signal line from the damage caused by ESD and other transients.

2. Features and benefits

- ESD protection of one line
- ESD protection up to 30 kV
- IEC 61000-4-5; (surge); I_{PPM} = 2.5 A
- Rated peak pulse power: P_{PPM} = 150 W
- Ultra low leakage current: I_{RM} < 1 nA
- Qualified according to AEC-Q101 and recommended for use in automotive applications

3. Applications

- computers and peripherals
- audio and video equipment
- cellular handsets and accessories
- portable electronics
- communication systems

4. Quick reference data

Symbol	Parameter	Conditions	Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C	-	-	36	V
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C	-	18	30	pF



5. Pinning information

Table 2	. Pinning info	ormation		
Pin	Symbol	Description	Simplified outline	Graphic symbol
1	К	cathode[1]	1 2	1 + 2
2	А	anode		
			SC-90 (SOD323F)	sym035

[1] The marking bar indicates the cathode.

6. Ordering information

Table 3. Ordering information

Type number Package				
	Name	Description	Version	
PESD36VS1UJ-Q		plastic, surface-mounted package; 2 leads; 1.7 mm x 1.25 mm x 0.7 mm body	SOD323F	

7. Marking

Table 4. Marking codes				
Type number	Marking code			
PESD36VS1UJ-Q	EA			

8. Limiting values

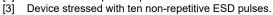
Table 5. Limiting values

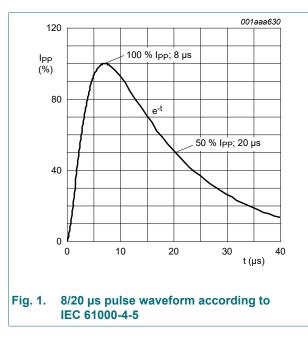
In accordance with the Absolute Maximum Rating System (IEC 60134).

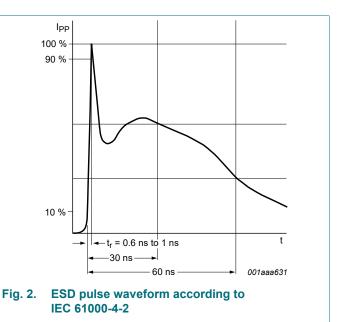
Symbol	Parameter	Conditions		Min	Max	Unit
P _{PPM}	rated peak pulse power	t _p = 8/20 μs	[1] [2]	-	150	W
I _{PPM}	rated peak pulse current		[1] [2]	-	2.5	А
Tj	junction temperature			-	150	°C
T _{amb}	ambient temperature			-55	150	°C
T _{stg}	storage temperature			-65	150	°C
ESD maximun	n ratings					•
V _{ESD}	voltago	IEC 61000-4-2; contact discharge	[2] [3]	-	30	kV
		IEC 61000-4-2; air discharge	[2]	-	30	kV

[1] Device stressed with ten non-repetitive ESD pulses (8/20 µs exponential decay waveform according to IEC 61000-4-5).

[2] Measured from pin 1 to pin 2.







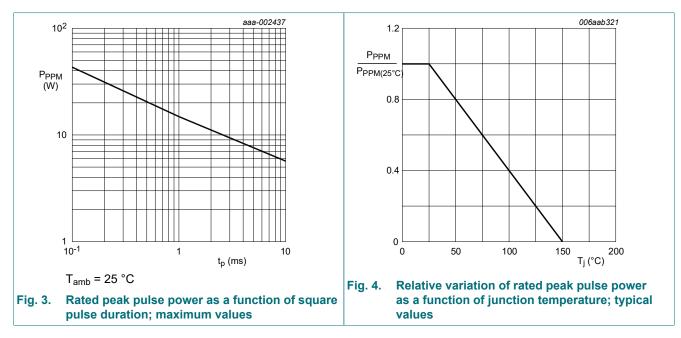
9. Characteristics

Table 6. Cha	racteristics						
Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V _{RWM}	reverse standoff voltage	T _{amb} = 25 °C		-	-	36	V
V _{BR}	breakdown voltage	I _R = 2 mA; T _{amb} = 25 °C		38.2	39	39.8	V
I _{RM}	reverse leakage current	V _R = 36 V; T _{amb} = 25 °C		-	1	10	nA
C _d	diode capacitance	f = 1 MHz; V _R = 0 V; T _{amb} = 25 °C		-	18	30	pF
V _{CL}	clamping voltage	I _{PP} = 1 A; T _{amb} = 25 °C	[1] [2]	-	-	58	V
		I _{PP} = 2.5 A; T _{amb} = 25 °C	[1] [2]	-	-	80	V
R _{dyn}	dynamic resistance	T _{amb} = 25 °C	[3]	-	9.5	-	Ω

Device stressed with 8/20 µs exponential decay waveform according to IEC 61000-4-5. [1]

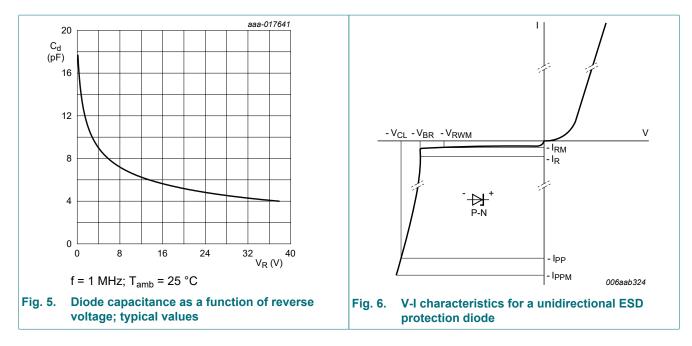
[2] [3] Measured from pin 1 to pin 2.

Non-repetitive current pulse, Transmission Line Pulse (TLP); square pulse; ANSI / ESD STM5.5.1-2008.



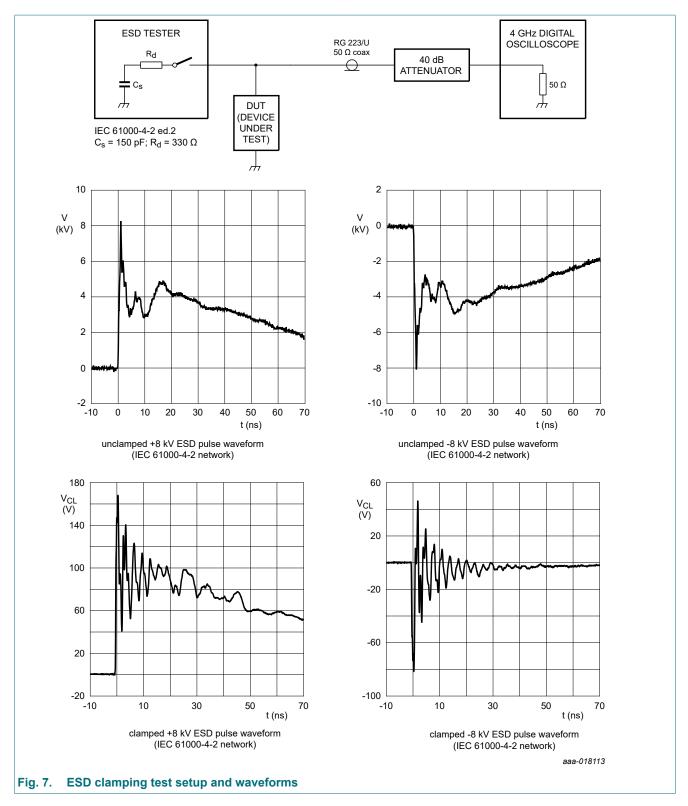
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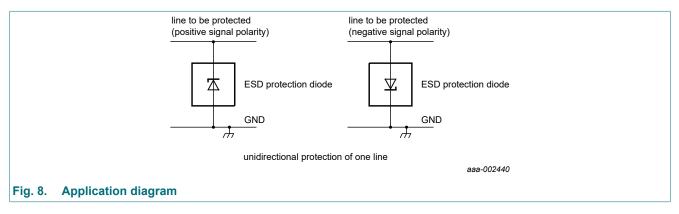
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10. Application information

The device is designed for protection of one unidirectional data or signal line from surge pulses and ESD damage. The device is suitable on lines where the signal polarities are either positive or negative with respect to ground.



Circuit board layout and protection device placement

Circuit board layout is critical for the suppression of ESD, Electrical Fast Transient (EFT) and surge transients. The following guidelines are recommended:

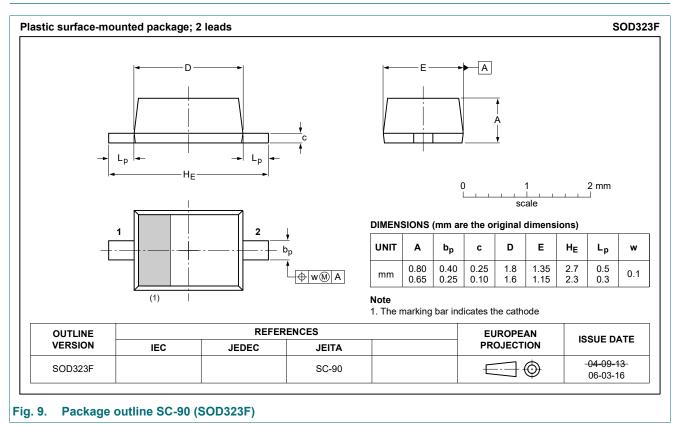
- 1. Place the device as close to the input terminal or connector as possible.
- 2. Minimize the path length between the device and the protected line.
- 3. Keep parallel signal paths to a minimum.
- 4. Avoid running protected conductors in parallel with unprotected conductors.
- 5. Minimize all Printed-Circuit Board (PCB) conductive loops including power and ground loops.
- **6.** Minimize the length of the transient return path to ground.
- 7. Avoid using shared transient return paths to a common ground point.
- 8. Use ground planes whenever possible. For multilayer PCBs, use ground vias.

11. Test information

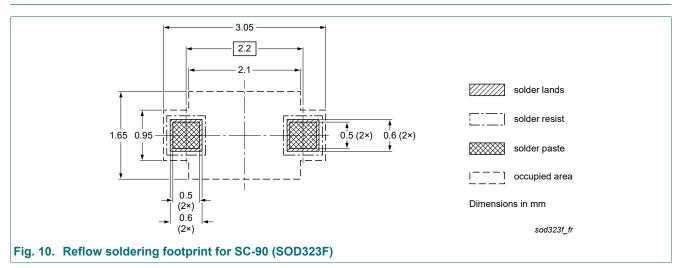
Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard Q101 - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

12. Package outline



13. Soldering



14. Revision history

Table 7. Revision history						
Data sheet ID	Release date	Data sheet status	Change notice	Supersedes		
PESD36VS1UJ-Q v.2	20220601	Product data sheet	-	PESD36VS1UJ-Q v.1		
Modifications	• Removed IEC 61643-321					
PESD36VS1UJ-Q v.1	20220503	Product data sheet	-	-		

15. Legal information

Data sheet status

Document status [1][2]	Product status [3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

 Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

[3] The product status of device(s) described in this document may have changed since this document was published and may differ in case of multiple devices. The latest product status information is available on the internet at <u>https://www.nexperia.com</u>.

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