**Product data sheet** 

## 1. General description

NPN medium power transistor series in a small SOT223 (SC-73) Surface-Mounted Device (SMD) plastic package.

**Table 1. Product overview** 

| Type number | Package  |       | NPN complement |
|-------------|----------|-------|----------------|
|             | Nexperia | JEITA |                |
| BCP55       | SOT223   | SC73  | BCP52          |
| BCP55-10    |          |       | BCP52-10       |
| BCP55-16    |          |       | BCP52-16       |

### 2. Features and benefits

- · High current
- · Three current gain selections
- · High power dissipation capability

## 3. Applications

- · Linear voltage regulators
- Power management
- Low-side switches
- MOSFET drivers
- · Battery-driven devices
- Amplifiers

### 4. Quick reference data

Table 2. Quick reference data

| Symbol          | Parameter                 | Conditions  |     | Min | Тур | Max | Unit |
|-----------------|---------------------------|---|-----|-----|-----|-----|------|
| $V_{CEO}$       | collector-emitter voltage | open base   |     | -   | -   | 60  | V    |
| Ic              | collector current         |   |     | -   | -   | 1   | А    |
| I <sub>CM</sub> | peak collector current    | single pulse; t <sub>p</sub> ≤ 1 ms                                     |     | -   | -   | 2   | А    |
| h <sub>FE</sub> | DC current gain           |   |     |     |     |     |      |
|                 | BCP55                     | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA T <sub>amb</sub> = 25 °C | [1] | 63  | -   | 250 |      |
|                 | BCP55-10                  |   | [1] | 63  | -   | 160 |      |
|                 | BCP55-16                  |   | [1] | 100 | -   | 250 |      |

[1] pulsed;  $t_p \le 300 \ \mu s$ ;  $\delta \le 0.02$ 



### 60 V, 1 A NPN medium power transistors

# 5. Pinning information

#### Table 3. Pinning

| Pin | Symbol | Description | Simplified outline               | Graphic symbol |
|-----|--------|-------------|----------------------------------|----------------|
| 1   | В      | base        | 4                                | С              |
| 2   | С      | collector   |                                  |                |
| 3   | E      | emitter     |                                  | B—             |
| 4   | С      | collector   | <b>∃</b> 1 <b>∃</b> 2 <b>∃</b> 3 | Ė              |
|     |        |             |                                  | sym123         |

# 6. Ordering information

#### **Table 4. Ordering information**

| Type number | Package | ackage   |         |  |  |  |  |  |
|-------------|---------|--|---------|--|--|--|--|--|
|             | Name    | Description  | Version |  |  |  |  |  |
| BCP55       | SC-73   | plastic surface-mounted package with increased heatsink; 4 leads | SOT223  |  |  |  |  |  |
| BCP55-10    |         |  |         |  |  |  |  |  |
| BCP55-16    |         |  |         |  |  |  |  |  |

# 7. Marking

#### Table 5. Marking

| Type number | Marking code |
|-------------|--------------|
| BCP55       | BCP55        |
| BCP55-10    | BCP55 /10    |
| BCP55-16    | BCP55 /16    |

#### 60 V, 1 A NPN medium power transistors

## 8. Limiting values

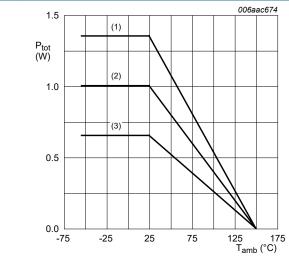
#### **Table 6. Limiting values**

In accordance with the Absolute Maximum Rating System (IEC 60134).

| Symbol           | Parameter                 | Conditions                          |     | Min | Max  | Unit |
|------------------|---------------------------|-------------------------------------|-----|-----|------|------|
| $V_{CBO}$        | collector-base voltage    | open emitter                        |     | -   | 60   | V    |
| V <sub>CEO</sub> | collector-emitter voltage | open base                           |     | -   | 60   | V    |
| $V_{EBO}$        | emitter-base voltage      | open collector                      |     | -   | 5    | V    |
| I <sub>C</sub>   | collector current         |                                     |     | -   | 1    | А    |
| I <sub>CM</sub>  | peak collector current    | single pulse; t <sub>p</sub> ≤ 1 ms |     | -   | 2    | А    |
| I <sub>B</sub>   | base current              |                                     |     | -   | 0.3  | А    |
| I <sub>BM</sub>  | peak base current         | single pulse; t <sub>p</sub> ≤ 1 ms |     | -   | 0.3  | А    |
| P <sub>tot</sub> | total power dissipation   | T <sub>amb</sub> ≤ 25 °C            | [1] | -   | 0.65 | W    |
|                  |                           |                                     | [2] | -   | 1.00 | W    |
|                  |                           |                                     | [3] | -   | 1.35 | W    |
| Tj               | junction temperature      |                                     |     | -   | 150  | °C   |
| T <sub>amb</sub> | ambient temperature       |                                     |     | -55 | 150  | °C   |
| T <sub>stg</sub> | storage temperature       |                                     |     | -65 | 150  | °C   |

- Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

  Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 1 cm<sup>2</sup>.
- Device mounted on an FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 6 cm<sup>2</sup>. [3]



- (1) FFR4 PCB, mounting pad for collector 6 cm<sup>2</sup>
- (2) FFR4 PCB, mounting pad for collector 1 cm<sup>2</sup>
- (3) FR4 PCB, standard footprint

Fig. 1. Power derating curves SOT223

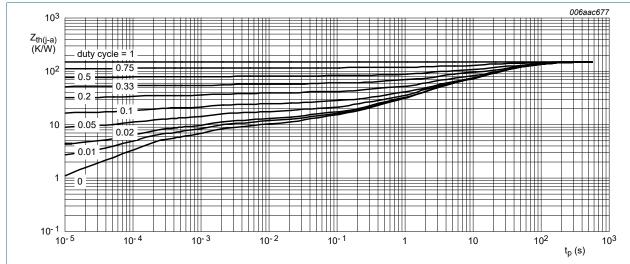
#### 60 V, 1 A NPN medium power transistors

## 9. Thermal characteristics

**Table 7. Thermal characteristics** 

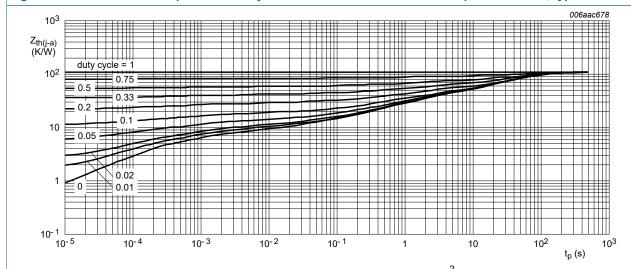
| Symbol                | Parameter  | Conditions  |     | Min | Тур | Max | Unit |
|-----------------------|--|-------------|-----|-----|-----|-----|------|
| R <sub>th(j-a)</sub>  | thermal resistance from junction to ambient      | in free air | [1] | -   | -   | 192 | K/W  |
|                       |  |             | [2] | -   | -   | 125 | K/W  |
|                       |  |             | [3] | -   | -   | 93  | K/W  |
| R <sub>th(j-sp)</sub> | thermal resistance from junction to solder point |             |     | -   | -   | 16  | K/W  |

- [1] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.
- [2] Device mounted on an FR4 PCB, single-sided copper, tin-plated; monting pad for collector 1 cm<sup>2</sup>.
- [3] Device mounted on an FR4 PCB, single-sided copper, tin-plated; monting pad for collector 6 cm<sup>2</sup>.



FR4 PCB, single-sided, tin-plated and standard footprint

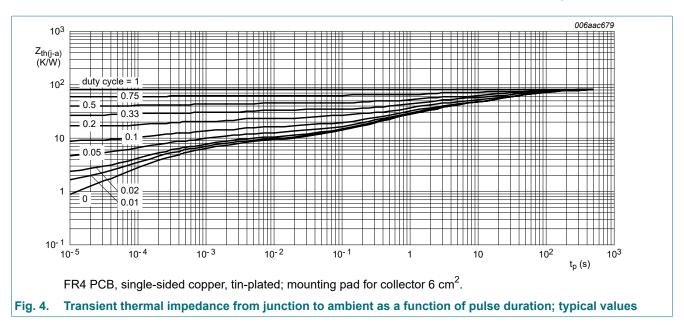
Fig. 2. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values



FR4 PCB, single-sided copper, tin-plated; mounting pad for collector 1 cm<sup>2</sup>.

Fig. 3. Transient thermal impedance from junction to ambient as a function of pulse duration; typical values

#### 60 V, 1 A NPN medium power transistors



### 60 V, 1 A NPN medium power transistors

## 10. Characteristics

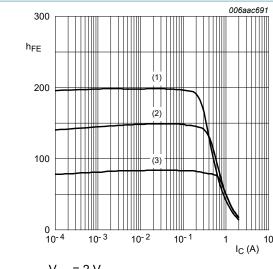
#### **Table 8. Characteristics**

| Symbol               | Parameter                            | Conditions   |     | Min | Тур | Max | Unit |
|----------------------|--------------------------------------|--|-----|-----|-----|-----|------|
| V <sub>(BR)CBO</sub> | collector-base<br>breakdown voltage  | $I_C = 100 \mu A; I_E = 0; T_{amb} = 25 °C$  |     | 60  | -   | -   | V    |
| V <sub>(BR)CEO</sub> | collector-emitter breakdown voltage  | I <sub>C</sub> = 2 μA; I <sub>B</sub> = 0 A; T <sub>amb</sub> = 25 °C                              |     | 60  | -   | _   | V    |
| V <sub>(BR)EBO</sub> | emitter-base<br>breakdown voltage    | I <sub>C</sub> = 0 A; I <sub>E</sub> = 100 μA  |     | 5   | -   | -   | V    |
| I <sub>CBO</sub>     | collector-base                       | V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>amb</sub> = 25 °C                             |     | -   | -   | 100 | nA   |
|                      | cut-off current                      | V <sub>CB</sub> = 30 V; I <sub>E</sub> = 0 A; T <sub>j</sub> = 150 °C                              |     | -   | -   | 10  | μA   |
| I <sub>EBO</sub>     | emitter-base cut-off current         | V <sub>EB</sub> = 5 V; I <sub>C</sub> = 0 A; T <sub>amb</sub> = 25 °C                              |     | -   | -   | 100 | nA   |
| h <sub>FE</sub>      | DC current gain                      |  |     |     |     | '   |      |
|                      | BCP55                                | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA; T <sub>amb</sub> = 25 °C                             | [1] | 63  | -   | -   |      |
|                      |                                      | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA; T <sub>amb</sub> = 25 °C                           | [1] | 63  | -   | 250 |      |
|                      |                                      | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C                           | [1] | 40  | -   | -   |      |
|                      | BCP55-10                             | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA; T <sub>amb</sub> = 25 °C                             | [1] | 63  | -   | -   |      |
|                      |                                      | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA; T <sub>amb</sub> = 25 °C                           | [1] | 63  | -   | 160 |      |
|                      |                                      | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C                           | [1] | 40  | -   | -   |      |
|                      | BCP55-16                             | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA; T <sub>amb</sub> = 25 °C                             | [1] | 63  | -   | -   |      |
|                      |                                      | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 150 mA; T <sub>amb</sub> = 25 °C                           | [1] | 100 | -   | 250 |      |
|                      |                                      | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C                           | [1] | 40  | -   | -   |      |
| h <sub>FE</sub>      | DC current gain                      | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 5 mA; T <sub>amb</sub> = 25 °C                             | [1] | 63  | -   | -   |      |
| h <sub>FE</sub>      | DC current gain                      | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C                           | [1] | 40  | -   | -   |      |
| V <sub>CEsat</sub>   | collector-emitter saturation voltage | I <sub>C</sub> = 500 mA; I <sub>B</sub> = 50 mA; T <sub>amb</sub> = 25 °C                          | [1] | -   | -   | 0.5 | V    |
| V <sub>BE</sub>      | base-emitter voltage                 | V <sub>CE</sub> = 2 V; I <sub>C</sub> = 500 mA; T <sub>amb</sub> = 25 °C                           | [1] | -   | -   | 1   | V    |
| C <sub>c</sub>       | collector capacitance                | $V_{CB} = 10 \text{ V}; I_E = i_e = 0 \text{ A}; f = 1 \text{ MHz}; $<br>$T_{amb} = 25 \text{ °C}$ |     | -   | 6   | -   | pF   |
| f <sub>T</sub>       | transition frequency                 | V <sub>CE</sub> = 5 V; I <sub>C</sub> = 50 mA; f = 100 MHz;<br>T <sub>amb</sub> = 25 °C            |     | 100 | 180 | -   | MHz  |

<sup>[1]</sup> pulsed;  $t_p \le 300 \ \mu s; \ \delta \le 0.02$ 

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### 60 V, 1 A NPN medium power transistors



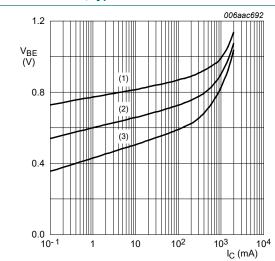
$$V_{CE} = 2 V$$

(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = -55$$
 °C

Fig. 5. DC current gain as a function of collector current; typical values

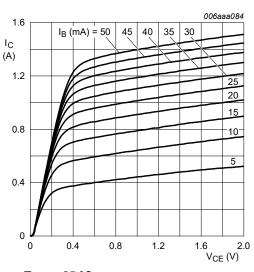


$$V_{CE} = 2 V$$

(1) 
$$T_{amb} = -55 \, ^{\circ}C$$

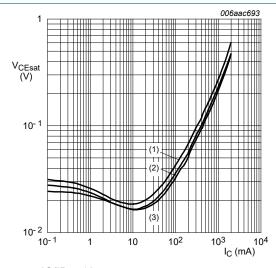
(3) 
$$T_{amb} = 100 \, ^{\circ}C$$

Fig. 7. Base-emitter voltage as a function of collector current; typical values



 $T_{amb}$  = 25 °C

Fig. 6. Collector current as a function of collectoremitter voltage; typical values



(1) 
$$T_{amb} = 100 \, ^{\circ}C$$

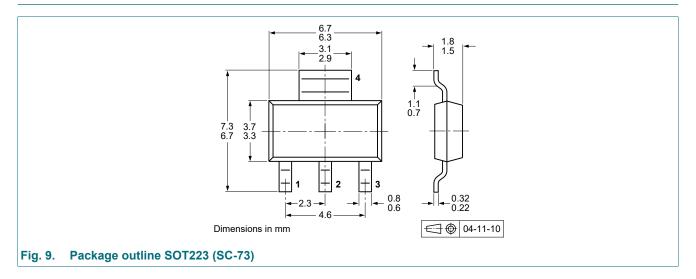
(2) 
$$T_{amb}$$
 = 25 °C

(3) 
$$T_{amb} = -55 \, ^{\circ}C$$

Fig. 8. Collector-emitter saturation voltage as a function of collector current; typical values

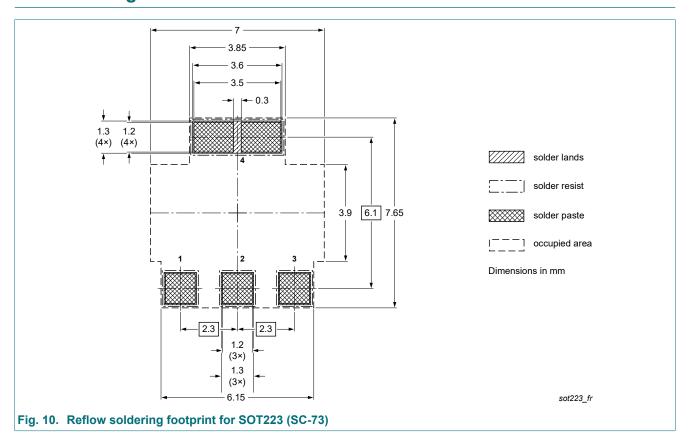
### 60 V, 1 A NPN medium power transistors

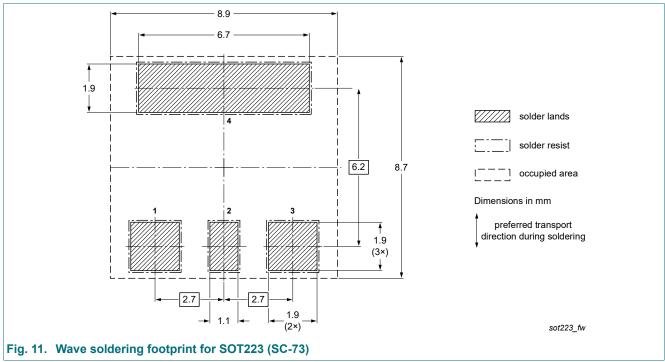
# 11. Package outline



#### 60 V, 1 A NPN medium power transistors

## 12. Soldering





### 60 V, 1 A NPN medium power transistors

# 13. Revision history

#### Table 9. Revision history

| Tubic 5. Itevision matery |   |                       |               |   |  |  |
|---------------------------|---|-----------------------|---------------|---|--|--|
| Data sheet ID             | Release date  | Data sheet status     | Change notice | Supersedes  |  |  |
| BCP55_SER v.9             | 20220701  | Product data sheet    | -             | BCP55_BCX55_BC55PA v.8                                  |  |  |
| Modifications:            | <ul> <li>Series data sheet describing several packages reduced to series data sheets per package.</li> <li>Package information removed.</li> <li>Product changed to non-automotive qualification. Please refer to nexperia.com for automotive (-Q) product alternative(s).</li> </ul> |                       |               |   |  |  |
| BCP55_BCX55_BC55PA v.8    | 20111024  | Product data sheet    | -             | BC637_BCP55_BCX55 v.7                                   |  |  |
| BC637_BCP55_BCX55 v.7     | 20070625  | Product data sheet    | -             | BC637_BCP55_BCX55 v.6                                   |  |  |
| BC637_BCP55_BCX55 v.6     | 20050218  | Product data sheet    | CPCN200405029 | BC635_637_639 v.4<br>BCP54_55_56 v.5<br>BCX54_55_56 v.4 |  |  |
| BC635_637_639 v.4         | 20011010  | Product Specification | -             | BC635_637_639 v.3                                       |  |  |
| BCP54_55_56 v.5           | 20030206  | Product Specification | -             | BCX54_55_56 v.4   |  |  |
| BCX54_55_56 v.4           | 20011010  | Product Specification | -             | BCX54_55_56 v.3   |  |  |

# 60 V, 1 A NPN medium power transistors

## 14. Legal information

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| Document status [1][2]         | Product<br>status [3] | Definition  |
|--------------------------------|-----------------------|---|
| Objective [short] data sheet   | Development           | This document contains data from the objective specification for product development. |
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### 60 V, 1 A NPN medium power transistors

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